

Tarlochan Sidhu - nominated by IEEE Canada

Tarlochan Sidhu taught and conducted research at the University of Saskatchewan, reaching tenure in 1997, and then joined the University of Western Ontario where he is today Professor and Chair of the Electrical & Computer Engineering Department. Tarlochan Sidhu also holds the prestigious position of Hydro One Research Chair in Power Systems.

Tarlochan has worked tirelessly to build a unique lab infrastructure and support students towards their research and graduate studies in the area of power system protection. He has considerable experience in a wide range of practical applications and implementation of new power system protection algorithms with international companies such as General Electric, Areva T&D, RTDS Technologies and Schweitzer Engineering Labs. His most significant work concerns the detection and location of arcing faults, and this has resulted in numerous awards such as the IEEE PSRC Prize Paper Award, the IEEE Power Engineering Society Working Group Award, and the IEE (UK) Sebastien Z De Ferranti Premium Award.

Tarlochan Sidhu has been extremely active with the technical activities of learned societies such as the IEEE, CIGRE and IEE. The depth of this involvement and his leadership are evidenced by such current roles as Founding Editor-in-Chief of the International Journal of Emerging Power Systems, Vice-Chair of the Relay Practices Subcommittee of IEEE Power System Relaying Committee, member of the Editorial Board of IEEE Power Engineering Letters and Editor of IEEE Transactions on Power Delivery, to name just a few. In addition, Tarlochan has contributed significantly to international conference organization. For these many and varied achievements, and for his leadership in research and academe in Canada and internationally, we are pleased to honour him this evening with an EIC Fellowship.

Ladies & gentlemen and Mr. President, please welcome Tarlochan Sidhu as a Fellow of the Engineering Institute of Canada.